

Guest Lecture Report

Organized by : Department of Computer Science & Engineering
Ramachandra College of Engineering

Name of the External Judge : Mr. P Ramesh Babu,
Associate Professor, Department of CSE,
Amrita Sai Institute of Science & Technology,
Paritala, Kanchikacherala.

Topic : Bus Architecture and Instruction Formats in CO

Venue : Lecture Hall C217

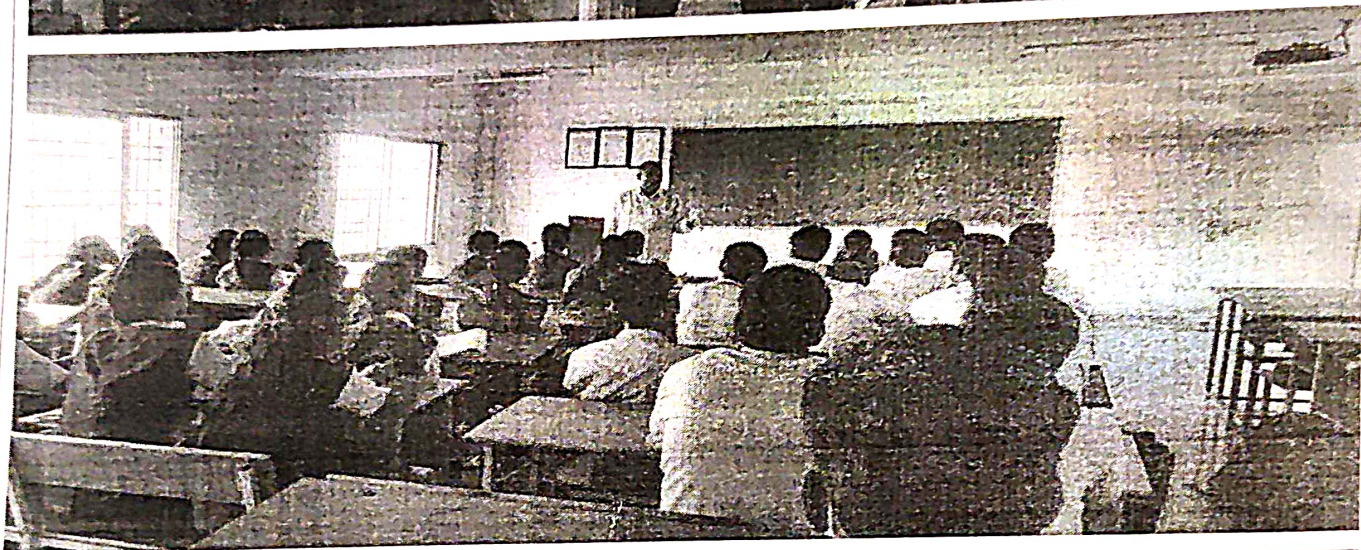
Date & Time : 27-05-2022 @ 01:00 PM to 03:00 PM

Branch	Year	Semester	No of Students Attended
CSE	I	II	65
Total No of Students Attended			65

Report -**1. Report in brief by Organizer / Coordinator / Convener:**

Computer Organization is one of the prominent subjects for CSE students. Bus Architecture and Instruction formats of a computer is playing a vital role for different flavors of Operating Systems. The expert was emphasized on "how the bus structure was implemented, uses of bus structure, importance of address bus, data bus, control bus". He also extended the lecture on instruction formats of a computer. He explained about "single instruction format, two instruction format and three instruction formats". Using these instruction formats, how operations on data will be performed? How architecture of computer can be implemented? What is Polish notation and Reverse Polish Notation? How these notations are useful during the evaluation of expressions in a specific language? The expert interacted with students and guide them to pave the path towards ultimate goal of B.Tech CSE programme.

All students are actively participated in the program and took suggestions from the expert.



3. Feedback from Participants:

Participants have felt very happy to attend this guest lecture. They expressed their sincere thanks and regards to conduct this kind of lectures to them.

[Signature]
Coordinator
20/05/2022

[Signature]
HOD